**Errata Sheet**

Reliabilty of Computer Systems and Networks: Fault Tolerance, Analysis and Design

by Martin L. Shooman, 2002 John Wiley & Sons, Inc. ISBN 0-471-29342-3

1. Page 74, Figure 2.12 X-axis should be labeled “Bit Error Probability, q”
2. Page 421, last sentence, 2nd paragraph … which are developed in Section A9
no such section in text
3. Page 421, Section B3.2, 4th sentence … a very good way to prevent these data
believe the word prevent 🡪 produce (??)
4. Page 440, Section B4.3 fails to state unit independence in the development of equation B62 from the equation B61
5. Page 449, Second sentence after (B76) should read:
Formal solution of Eq. (B74) proceeds in a similar manner.
6. Page 459, middle of page λ ≤ 10-4
Constraint for R(t) ≥ 0.90 is violated if λ ≥ 10-4 as shown on the page.
7. Page 113, Equation (3.62c), the failure rate should not have a prime which is shown correctly in the Laplace transform of the equation (3.63c).
8. Page 463 Figure B24, in a box labeled Algebraic manipulation, the denominator is incorrect, it should be (s+1)(s+2). [Tony Kolluri]
9. Page 134, Equation (3.80) The variables for downtime ( D ) and uptime ( U ) should all be annotated the same. The bar across the top of the variables must indicate average, it doesn’t imply complementation.
10. Page 87, Equation 3.8a Rb(p) 🡪 Rc(p) [Lori Pape]
11. Page 173, Equation 4.39b R5MR (t) λ3t3 + 2.5λ3λ + 2µ)t4 ………
12. Page 44, Table 2.4 title should say Parity-Bit Coding and not Parity-Rate Coding.
13. Page 281, Problem 5.12 (b) Plot the reliability function versus time from the start of operations/software release – after 4, 8, and 12 months of debugging.
14. Page 239, Equation 5.30a Et should be ET (typo ?)
15. e-Book, page 116 It has been reported that Figure 3.15 in the e-Book edition is not the same as the **correct** figure in the textbook. The feedback loop on S0 should be 1 – ( λ’ + λ’’ ) Δ t as in the textbook.
16. Page 469 Equation (B 106) is for MTTF and not MTTR as shown.
17. Page 51, 2nd paragraph, 3rd sentence ***yields c3’ c2’ and c1’ = 110 for the new check bits*** should read ***yields c1’ c2’ and c3’ = 110 for the new check bits***. (Tarun Sharma)
18. Page 455, Figure B18 has the wrong description since it is for a single component WITHOUT repair as described in Section B6.3 and essentially is the same as Figure B14 with z(t) = λ
Page 455 middle of page, *Similarly for Fig B18(b)* should read ***Similarly for Fig B17(b)***